Fig.1

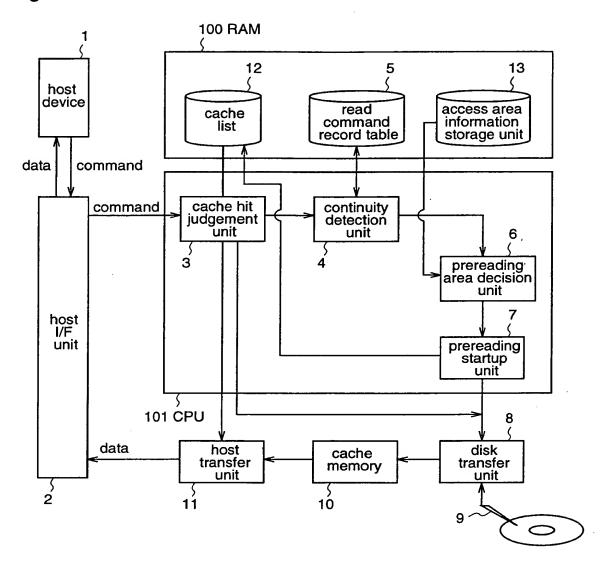
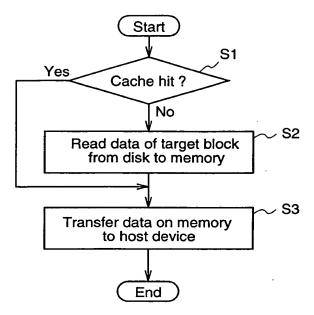
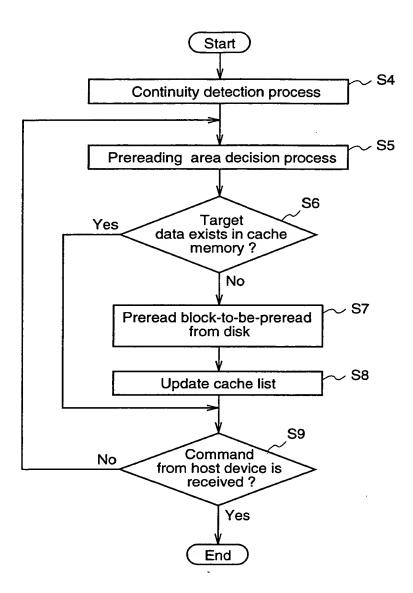


Fig.2



OSSESTI OSSES

Fig.3



DOWSOHLL CECYCE

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Fig.4

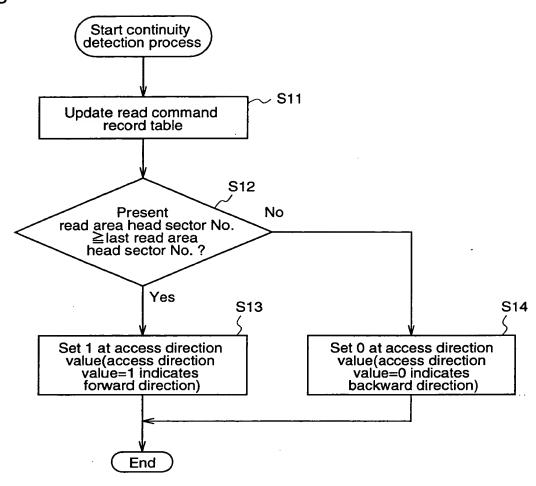


Fig.5

G present read area head sector No. present read area size Н last read area head sector No. Α last read area size В last-but-one read area head C sector No. D last-but-one read area size present access direction value ı last access direction value Ε

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Fig.6

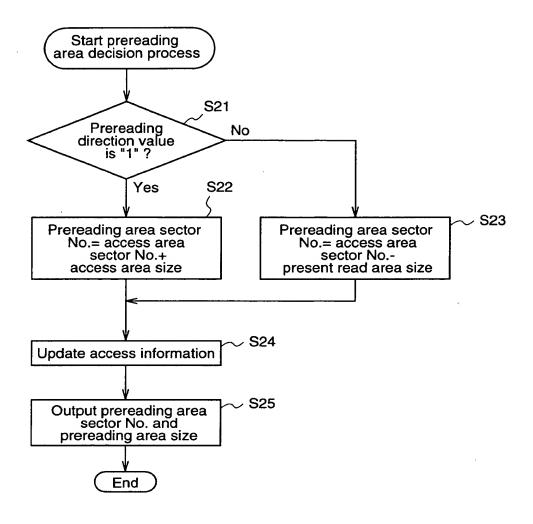


Fig.7

access area head sector No.	Q
access area size	R

Fig.8

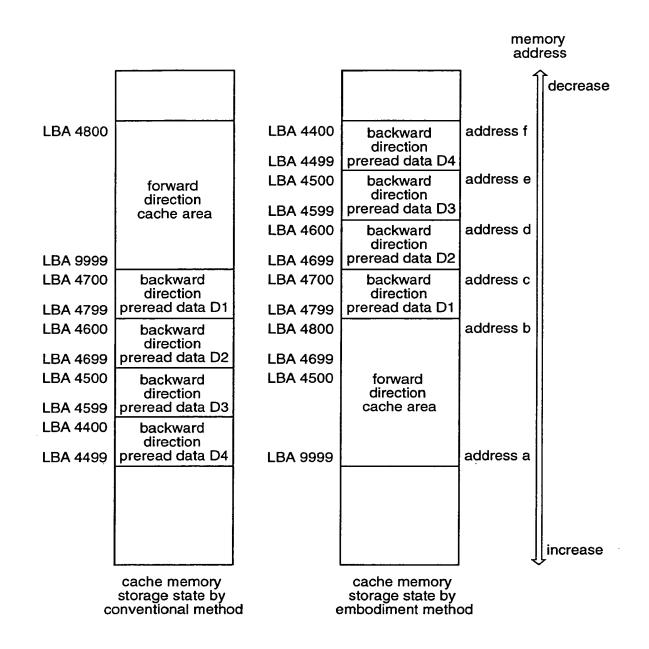
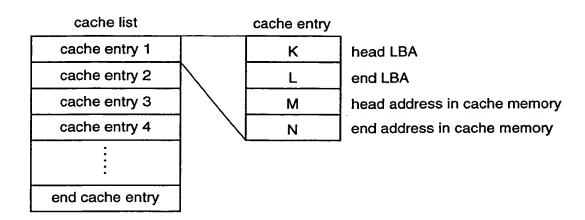


Fig.9



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Fig.10

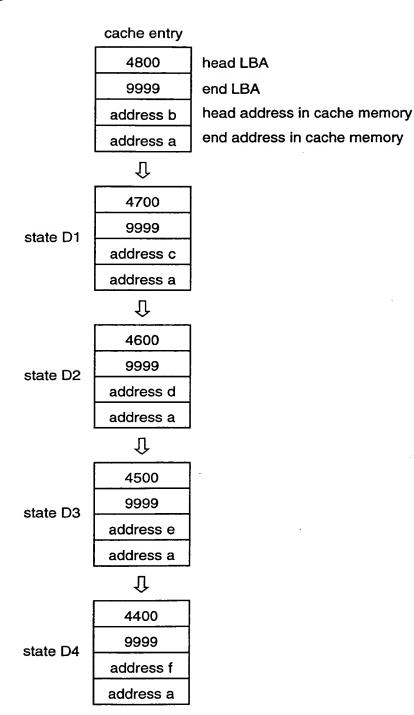


Fig.11

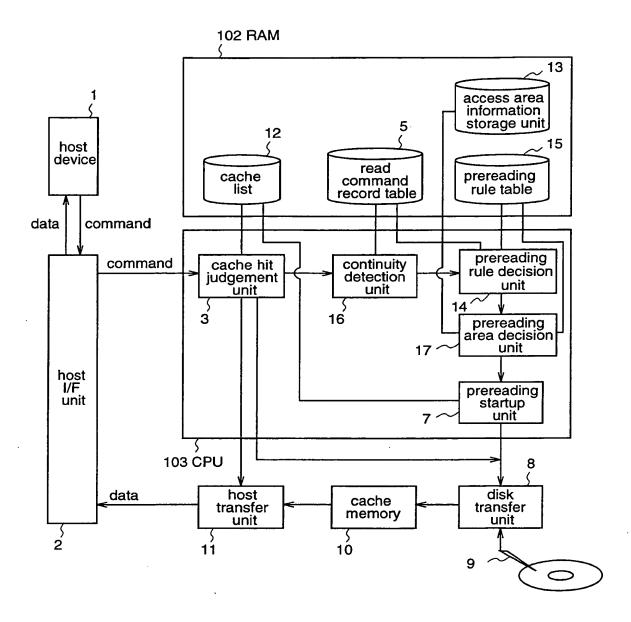


Fig.12

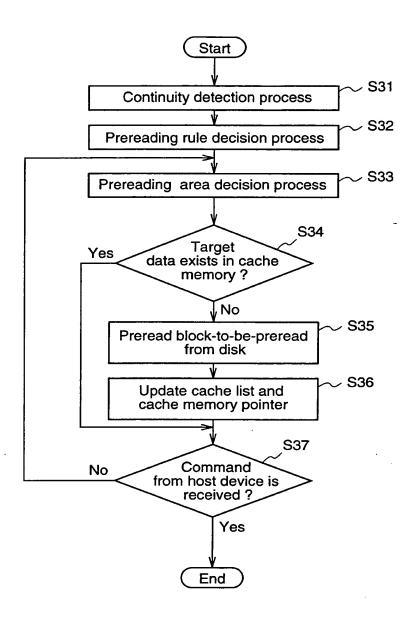


Fig.13

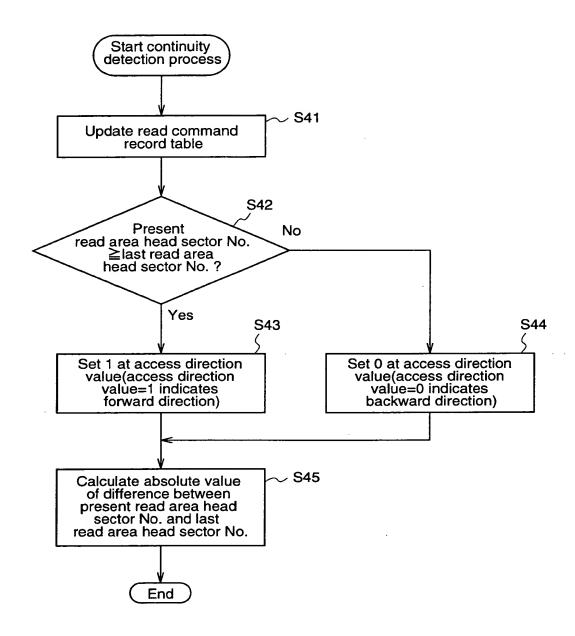


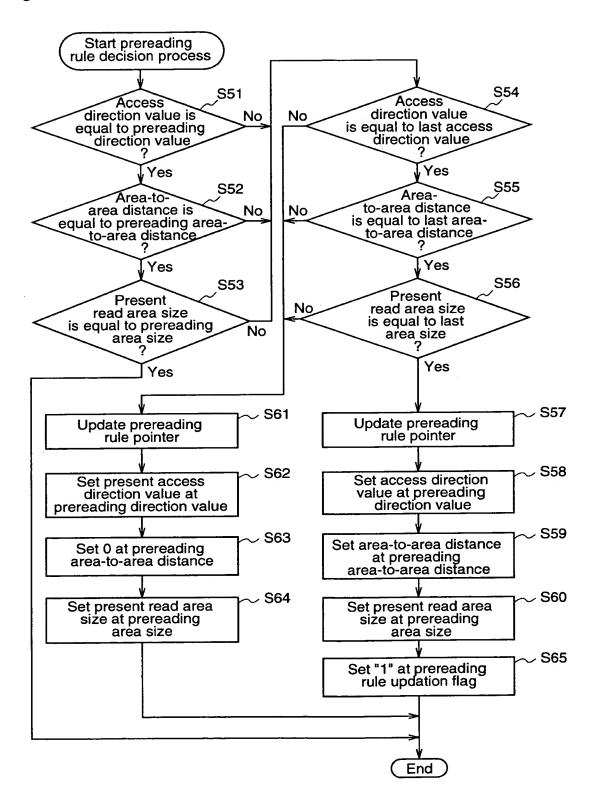
Fig.14

present read area head sector No.

present read area size
last read area head sector No.
last read area size
last-but-one read area head
sector No.
last-but-one read area size
present access direction value
present area-to-area distance
last area-to-area distance

G
Н
Α
В
C D
D
ı
J
E
F

Fig.15



prereading

Fig.16

rule table rule entry X_0 Wo prereading rule entry 0 Y_0 W₁ prereading rule entry 1 distance Z_0 W2 prereading rule entry 2 prereading rule entry 3 W3 X_1 prereading rule entry 4 W4 Y_1 prereading rule entry 5 **W5** distance Z_1 Т prereading rule updation flag U prereading rule pointer

prereading

prereading direction value prereading area-to-area prereading area size

prereading direction value prereading area-to-area prereading area size

Fig.17

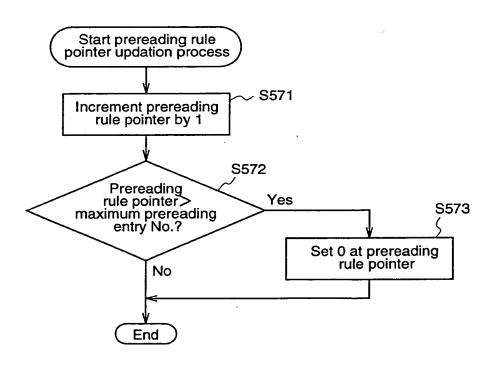


Fig.18

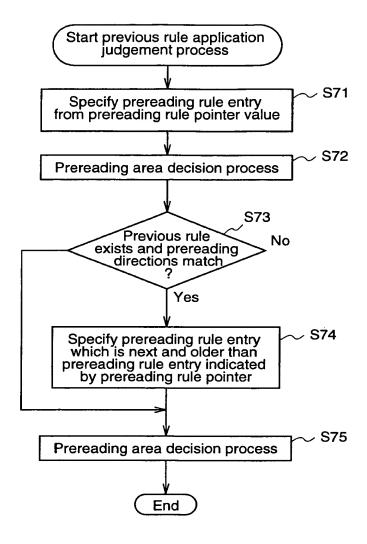


Fig.19

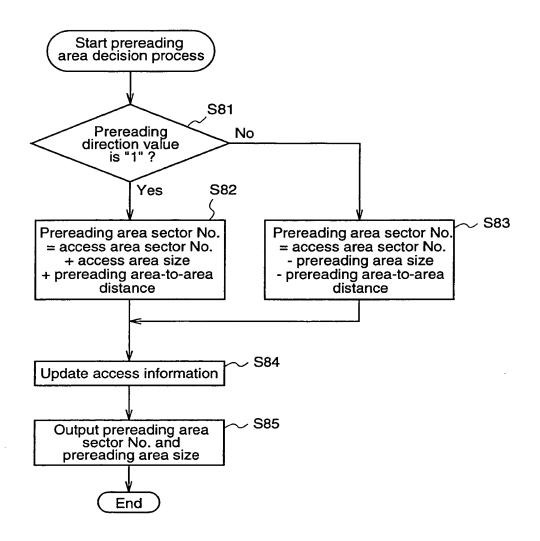


Fig.20

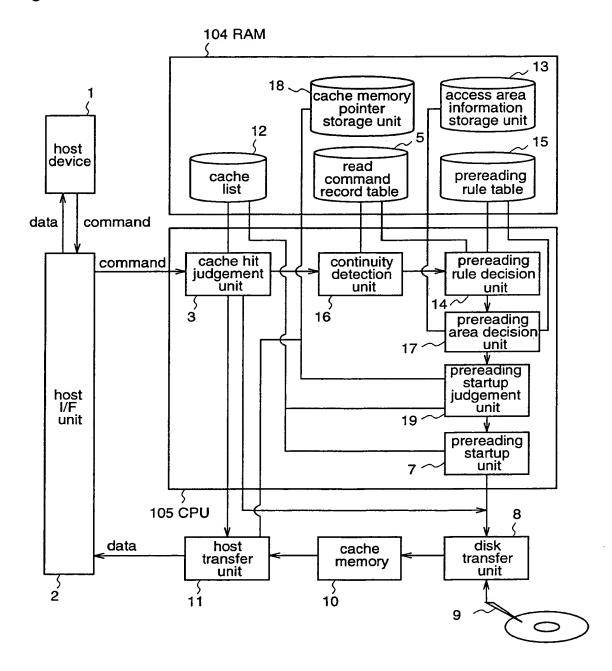


Fig.21

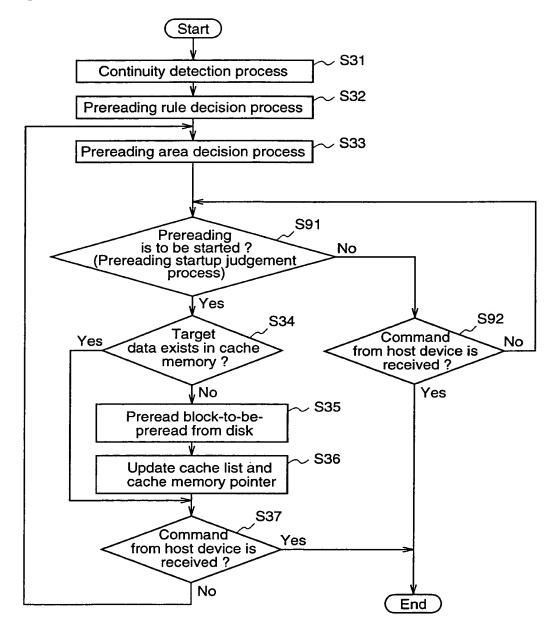


Fig.22

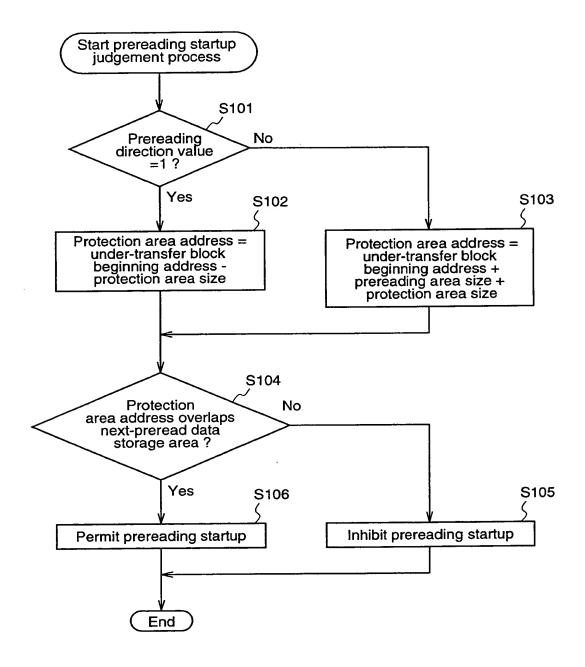


Fig.23

under-transfer address	0
next-preread data storage start address	Р

Fig.24

